

In the Specification:

Please amend the specification as follows:

Please amend paragraph 2 as follows:

[2] ~~Embodiments of The the~~ present invention ~~relates relate~~ to a receiver of digital signals originated from very different sources.

Please amend paragraph 17 as follows:

[17] Moreover, although advantageous under several ~~aspects~~~~embodiments~~, known solutions have technological limitations penalizing the industry cost, for example not allowing the CMOS technology implementation thereof.

Please amend paragraph 22 as follows:

[22] An idea underlying an ~~aspect~~~~embodiment~~ of the present invention is to perform, before sending the signals to the hysteresis comparator, an ALL-INPUT/single-ended conversion.

Please amend paragraph 23 as follows:

[23] Based on this idea the technical problem is solved according to one ~~aspect~~~~embodiment~~ of the present invention by a receiver essentially comprising a converter from an ALL-INPUT signal to an intermediate signal, for example of the trapezoidal type, as well as a traditional hysteresis comparator (comprising for example a Schmitt trigger). Advantageously according to an ~~aspect~~~~embodiment~~ of the invention, operating on the intermediate signal slope, together with the fixed comparison

thresholds of the hysteresis comparator, the receiver operates as a variable-threshold hysteresis comparator.

Please amend paragraph 24 as follows:

[24] More particularly, the technical problem is solved according to another aspect embodiment of the present invention by a signal receiver inserted between a first and a second voltage reference and having a first and a second input terminal effective to receive differential signals and an output terminal effective to provide a converted signal, characterized in that it comprises a conversion stage inserted between said first and second voltage references and connected between said first and second input terminals of said signal receiver and an input terminal of an hysteresis comparator, connected in turn to said output terminal of said signal receiver, said conversion stage performing a conversion from any input signal received on respective input terminals to an intermediate signal provided on an output terminal and suitable for reception by said hysteresis comparator.